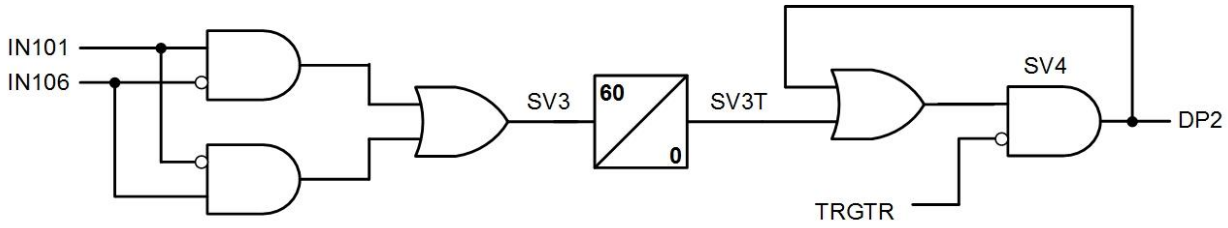
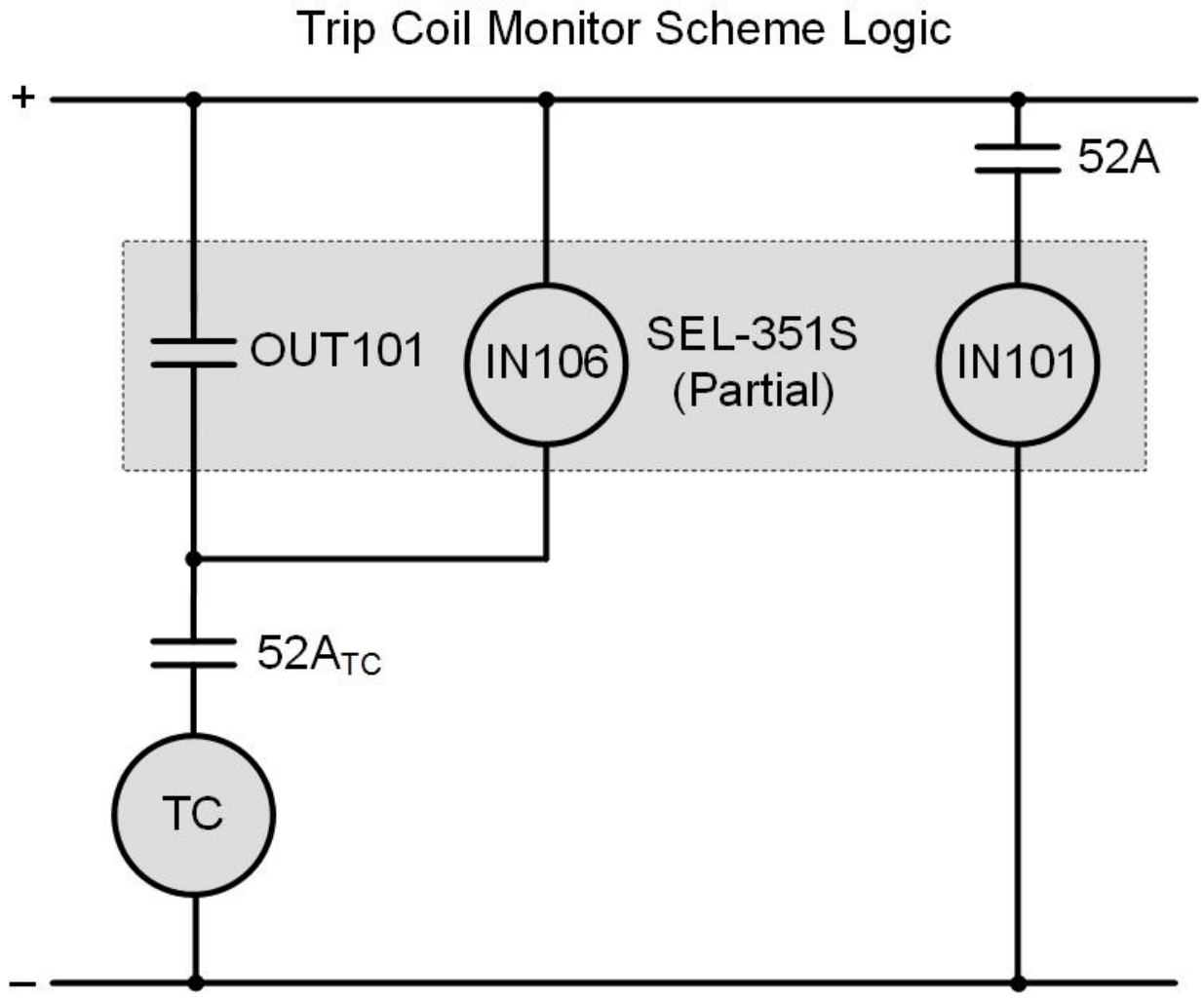
CBT 104: Understanding SEL Relay Logic

For use with questions 7 through 12 in Section 7: Final Knowledge Assessment.



Trip Coil Monitor Logic



Equation	Function
52A = IN101	**Input**: IN101 is assigned to 52A in default logic.
SV3 = IN101 * !IN106 + !IN101 * IN106	**Variable**: SC3 is an exclusive OR of input IN101 and IN106. IN106 is wired to the bottom of the trip output-OUT101. With the breaker closed, both IN101 and IN106 will be asserted. If either input is not asserted, then a problem exists.
SV3PU = 60.0 SV3D0 = 0.0	**Timer Settings**: If the problem persists for 60 cycles, SV3T asserts and then SV4 also asserts. There is no dropout delay; SV3D0 setting = 0.
SV4 = (SV4 + SV3T(* !TRGTR)	**Display Seal-in**: SV4 acts as a seal-in and is reset by pressing the {TARGET RESET} pushbutton. (TRGTR is the TARGET RESET Relay Word bit)
DP2 = SV4 DP2_1 = TRIP COIL FAIL DP2_0 = NA	**Display Settings**: The timed output SV3T is the alarm point.